

WEST

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15 and ((711/\$ or 714/\$).ccls.)	48

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L6

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Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L6</u>	15 and ((711/\$ or 714/\$).ccls.)	48	<u>L6</u>
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<u>L4</u>	L3 and (invalidat\$8 with (snoop\$4 or examin\$8 or monitor\$4 or watch\$4))	261	<u>L4</u>
<u>L3</u>	L2 and l1	834	<u>L3</u>
<u>L2</u>	bus with (snoop\$4 or examin\$8 or monitor\$4 or watch\$4)	16909	<u>L2</u>
<u>L1</u>	cache near4 ("L3" or third or three)	2618	<u>L1</u>

END OF SEARCH HISTORY

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L14</u>	L13 and ((parity or error or fault or fail\$4) with (((("L1" or (level adj one) or (on-chip) or internal) near6 cache) or ((processor or cpu) near4 (cache or memory))))	12	<u>L14</u>
<u>L13</u>	L12 and (bus with (snoop\$4 or examin\$8 or monitor\$4 or watch\$4))	87	<u>L13</u>
<u>L12</u>	L11 and ((invalidat\$4 or eras\$4 or delet\$4 or clear\$4) with (((("L1" or (level adj one) or (on-chip) or internal) near6 cache) or ((processor or cpu) near4 (cache or memory))))	147	<u>L12</u>
<u>L11</u>	cache near6 ("L3" or ((third or three) near4 level))	703	<u>L11</u>
<u>L10</u>	L8 and l5	10	<u>L10</u>
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<u>L8</u>	L2 same (parity or error or fault or fail\$4)	76	<u>L8</u>
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<u>L6</u>	L2 same invalidat\$4	978	<u>L6</u>
<u>L5</u>	L4 and (bus with (snoop\$4 or examin\$8 or monitor\$4 or watch\$4))	194	<u>L5</u>
<u>L4</u>	L3 and (cache near4 ("L3" or third or three))	285	<u>L4</u>
<u>L3</u>	L2 and (invalidat\$4 with (writ\$4 or stor\$4 or updat\$4 or modifi\$8))	834	<u>L3</u>
<u>L2</u>	invalidat\$4 with (((("L1" or (level adj one) or (on-chip) or internal) near6 cache) or ((processor or cpu) near4 (cache or memory))))	978	<u>L2</u>
<u>L1</u>	(((((bus with (snoop\$4 or examin\$8 or monitor\$4 or watch\$4))and (cache near4 ("L3" or third or three)))and (invalidat\$8 with (snoop\$4 or examin\$8 or monitor\$4 or watch\$4)))and (parity near8 (error or fail\$4 or fault))) and ((711/\$ or 714/\$).ccls.))	48	<u>L1</u>

END OF SEARCH HISTORY